

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	[STRUCTURE AND METHOD FOR REDUCING SOURCE LINE RESISTANCE OF LIGHT EMITTING DIODE]	
Application Number :		
Date :		
First Named Applicant: Mr. Jen-Yi Hu		
Confirmation Number:		
Attorney Docket Number: 10070-US-PA		
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Submitted By:	Elec. Sign.	Sign. Capacity
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Documents being submitted:	Files
us-fee-sheet	10070-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-assignment	10070-usassn.xml us-assignment.xsl us-assignment.dtd 10070assignment1.tif 10070assignment2.tif
us-request	10070-usrequ.xml us-request.dtd us-request.xsl
application-body	10070-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd 10070us0001.tif 10070us0002.tif 10070us0003.tif 10070us0004.tif 10070us0005.tif
us-declaration	10070declaration1.tif
us-declaration	10070declaration2.tif
Comments	
Please be kindly advised that the indicated fees of this EFS application is intended to be paid by credit card. However, in the event that the payment through credit card is unsuccessful, the Commissioner is authorized to	

charge the required fees in connection with this application to: Deposit Account No. 50-2620 (Order No. 10070-US-PA), Deposit Account Name: Jianq Chyun Intellectual Property Office, Authorized User Name: Belinda Lee, E-mail: belinda@jcipgroup.com.tw; usa@jcipgroup.com.tw